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Subject: 6502 UCODE INTERPRETER DISASSEMBLY WANTED,,, (G  
Posted by [Anonymous](#) on Sat, 22 Aug 2015 16:21:32 GMT

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Originally posted by: richardebaybroadhurst

No micro code, just a hardwired matrix of instruction / what to do per clock.

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Subject: Re: 6502 UCODE INTERPRETER DISASSEMBLY WANTED,,, (G  
Posted by [Anton Treuenfels](#) on Sat, 22 Aug 2015 18:00:44 GMT

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<richardebaybroadhurst@gmail.com> wrote in message  
news:f62f55d0-df05-41b2-9930-d61ad6a54507@googlegroups.com...  
> No micro code, just a hardwired matrix of instruction / what to do per  
> clock.

You might be better off checking at [www.6502.org](http://www.6502.org) A number of people there  
have experience with both hardware and software emulations of the 6502  
family chips.

- Anton Treuenfels

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Subject: Re: 6502 UCODE INTERPRETER DISASSEMBLY WANTED,,, (G  
Posted by [Dombo](#) on Wed, 26 Aug 2015 17:35:24 GMT

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Op 22-Aug-15 om 18:21 schreef richardebaybroadhurst@gmail.com:  
> No micro code, just a hardwired matrix of instruction / what to do per clock.

Depending on what your objectives are (not quite clear to me), this  
might be interesting for you: <http://www.visual6502.org/JSSim/expert.html>

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